# TOSHIBA

#### TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX138F,TC74LCX138FN,TC74LCX138FT,TC74LCX138FK

Low-Voltage 3-to-8 Line Decoder with 5-V Tolerant Inputs and Outputs

The TC74LCX138 is a high-performance CMOS 3-to-8 decoder. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V) V<sub>CC</sub> applications, but it could be used to interface to 5-V supply environment for inputs.

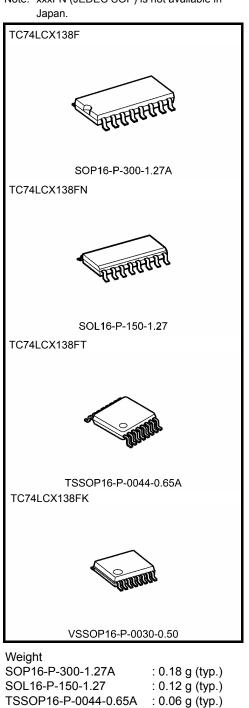
When the device is enabled, 3 binary select inputs (A, B and C) determine which one of the outputs  $(\overline{Y}0 \cdot \overline{Y}7)$  will go low. When enable input G1 is held low or either  $\overline{G}2A$  or  $\overline{G}2B$  is held high, decoding function is inhibited and all outputs go high.

G1,  $\overline{G}2A$ , and  $\overline{G}2B$  inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

All inputs are equipped with protection circuits against static discharge.

# Features

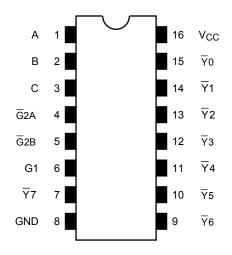
- Low-voltage operation: V<sub>CC</sub> = 2.0 to 3.6 V
- High-speed operation:  $t_{pd} = 6.0 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current:  $|I_{OH}|/I_{OL} = 24 \text{ mA} (\min) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 138 type



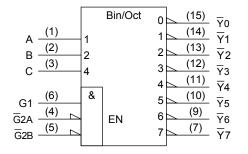
VSSOP16-P-0030-0.50

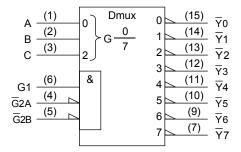
: 0.02 g (typ.)

# Pin Assignment (top view)



# **IEC Logic Symbol**





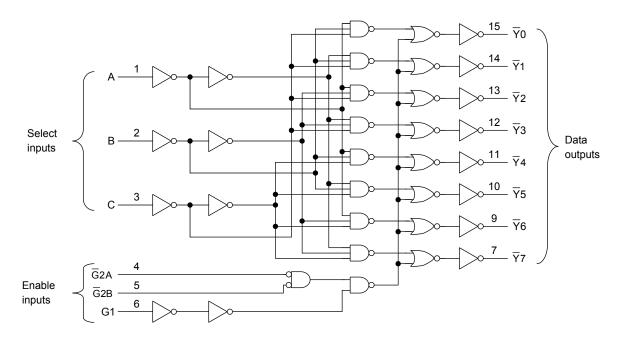
# **Truth Table**

	Inputs					Outputs								
Enable		Select		- Y0	T <sub>1</sub>	T <sub>2</sub>	¥3	¥4	¥5	¥6	Ŧ7	Selected Output		
G1	G2A	G2B	С	В	А	10	TI	12	13	14	15	10	17	
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Ψ0
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	<u>¥</u> 1
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	¥2
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	н	Н	Н	<u>¥</u> 3
Н	L	L	Н	L	L	Н	Н	Н	Н	L	н	Н	Н	$\overline{Y}4$
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	¥5
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	<del>Υ</del> 6
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	¥7

X: Don't care

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# System Diagram



# Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	–0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	–0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)	V	
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)		
Input diode current	IIK	-50	mA	
Output diode current	I <sub>OK</sub>	±50 (Note 4)	mA	
DC output current	IOUT	±50	mA	
Power dissipation	PD	180	mW	
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature	T <sub>stg</sub>	–65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state.  $I_{\mbox{OUT}}$  absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 

# **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	Vee	2.0 to 3.6	V	
Tower supply voltage	vcc	1.5 to 3.6 (Note 2)	v	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	Vour	0 to 5.5 (Note 3)	V	
Output voltage	V <sub>CC</sub> 2.0 to 3.6        V <sub>IN</sub> 0 to 5.5		v	
Output current	lau/lau	±24 (Note 5)	mA	
Output current	'OH/'OL	±12 (Note 6)	ША	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

- Note 2: Data retention only
- Note 3:  $V_{CC} = 0 V$
- Note 4: High or low state
- Note 5:  $V_{CC} = 3.0$  to 3.6 V
- Note 6:  $V_{CC} = 2.7$  to 3.0 V
- Note 7:  $V_{IN}=0.8$  to 2.0 V,  $V_{CC}=3.0$  V

#### **Electrical Characteristics**

#### DC Characteristics (Ta = -40 to 85°C)

Characterist	ics	Symbol	Test Co	V <sub>CC</sub> (V)	Min	Max	Unit	
Input voltage	H-level	VIH			2.7 to 3.6	2.0	_	V
Input voltage	L-level	V <sub>IL</sub>	_	-	2.7 to 3.6	_	0.8	v
			$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -100 \ \mu A$	2.7 to 3.6	V <sub>CC</sub> -0.2	_	V
	H-level	V <sub>OH</sub>		I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	—	
Output voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
			$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 100 \ \mu A$	2.7 to 3.6	—	0.2	
	L-level	Vol		$I_{OL} = 12 \text{ mA}$	2.7	—	0.4	
	L-IEVEI	VOL		I <sub>OL</sub> = 16 mA	3.0	—	0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	$V_{IN} = 0$ to 5.5 V		2.7 to 3.6	—	±5.0	μA
Power-off leakage current		IOFF	$V_{IN}/V_{OUT} = 5.5 V$		0	—	10.0	μA
Quiescent supply current			$V_{IN} = V_{CC}$ or GND		2.7 to 3.6	—	10.0	
		ICC	$V_{IN} = 3.6$ to 5.5 V	2.7 to 3.6	—	±10.0	μA	
Increase in Icc per inp	ut	$\Delta I_{CC}$	$V_{IH} = V_{CC} - 0.6 \ V$		2.7 to 3.6	_	500	

#### AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7	_	7.0	ns
(A, B, C- <u>Y</u> )	t <sub>pHL</sub>		$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0	
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7	_	8.0	ns
(G1- <u>Y</u> )	t <sub>pHL</sub>		$\textbf{3.3}\pm\textbf{0.3}$	1.5	7.0	
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7	_	7.0	ns
( <u>G</u> 2 - <u>Y</u> )	t <sub>pHL</sub>		$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0	
	t <sub>osLH</sub>	(Note)	2.7	_	_	20
Output to output skew	t <sub>osHL</sub>	(NOLE)	$\textbf{3.3}\pm\textbf{0.3}$	—	1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$ 

#### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5 \text{ ns}$ , $C_L = 50 \text{ pF}$ , $R_L = 500 \Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic $V_{OL}$	V <sub>OLP</sub>	$V_{IH} = 3.3 V, V_{IL} = 0 V$	3.3	0.8	V
Quiet output minimum dynamic $V_{OL}$	V <sub>OLV</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

#### **Capacitive Characteristics (Ta = 25°C)**

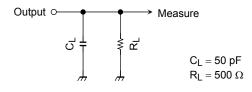
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_	3.3	7	pF
Output capacitance	C <sub>OUT</sub>	—	0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz (Not	e) 3.3	25	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

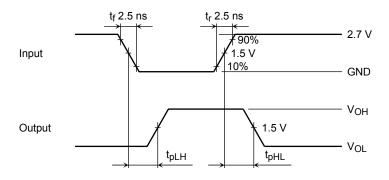
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

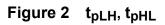
# **AC Test Circuit**





# AC Waveform



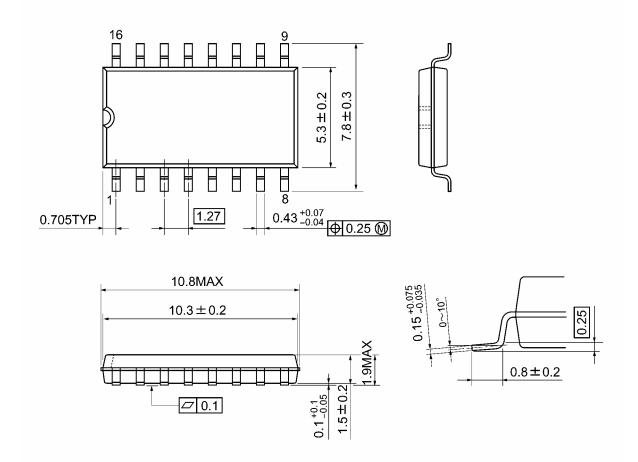




## **Package Dimensions**

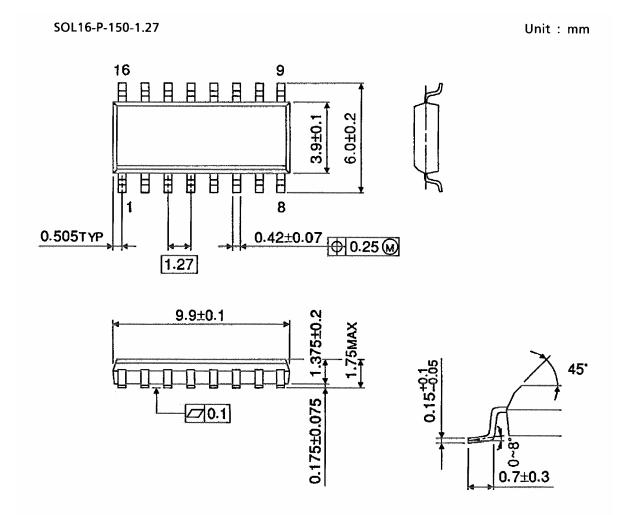
SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

# Package Dimensions (Note)



Note: This package is not available in japan.

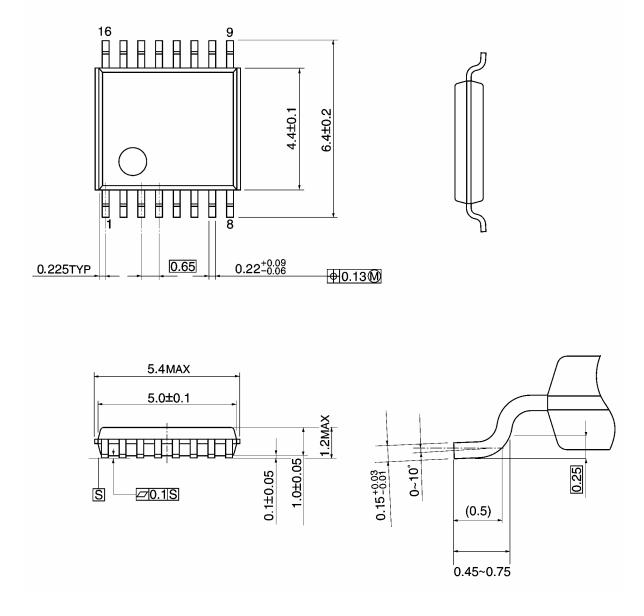
Weight: 0.12 g (typ.)

# **TOSHIBA**

# Package Dimensions

TSSOP16-P-0044-0.65A

Unit: mm



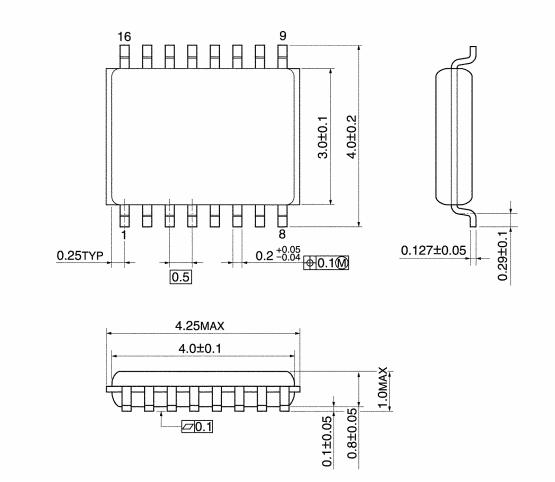
Weight: 0.06 g (typ.)

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# **Package Dimensions**

VSSOP16-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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20070701-EN GENERAL

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